

Fig. 1

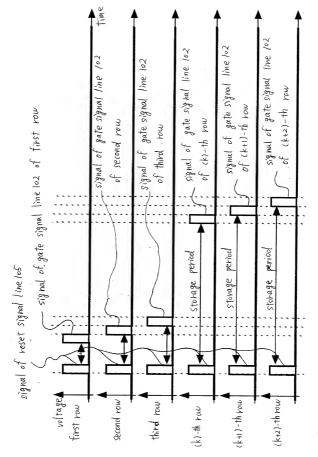


Fig. 2

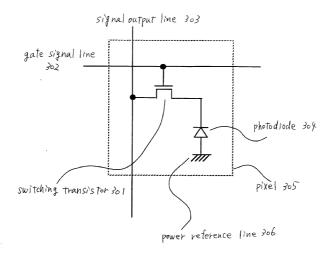


Fig. 3 Prior Art

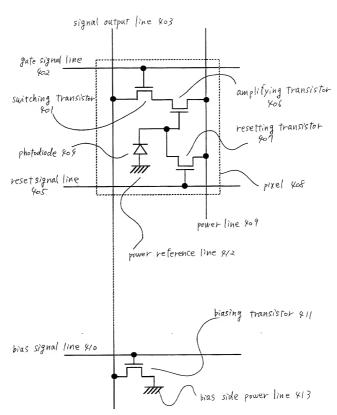


Fig. 4 Prior Art

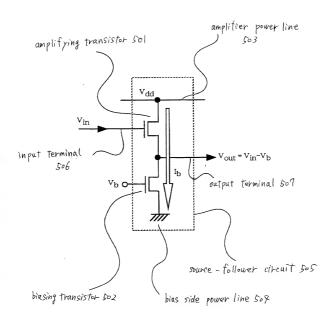
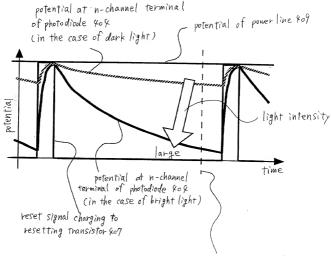
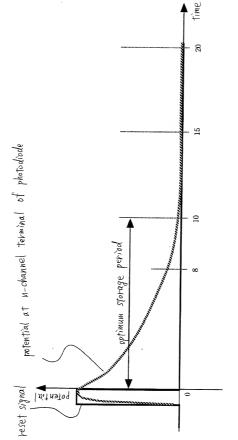


Fig. 5

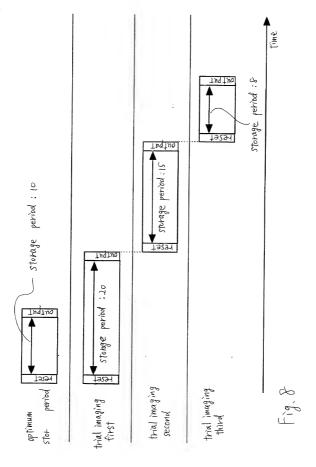


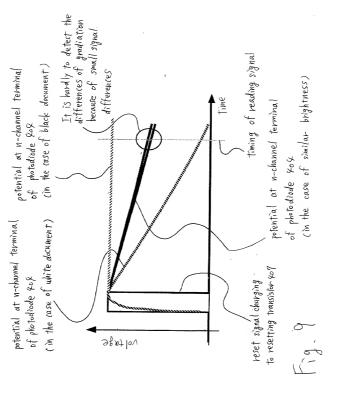
signal (potential at n-channel terminal of photodiode) is outputted at an instant of time

Fig. 6



F.9. 7





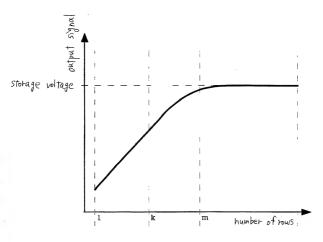
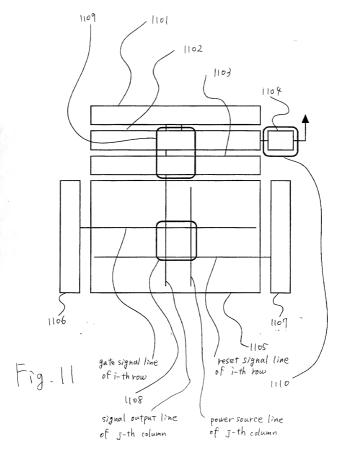


Fig. 10



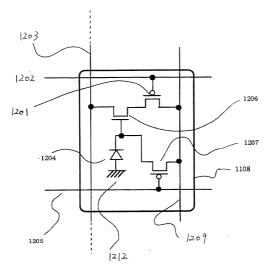


Fig. 12

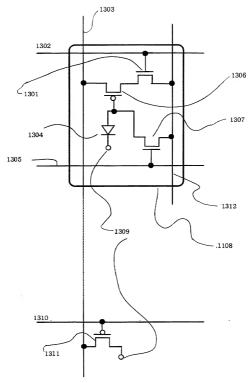


Fig. 13

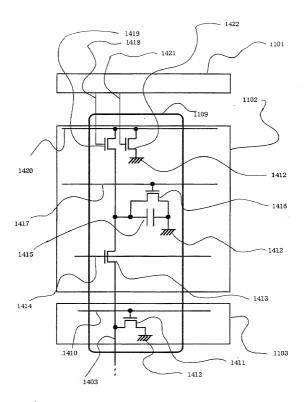
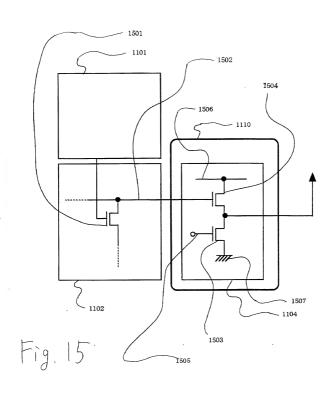
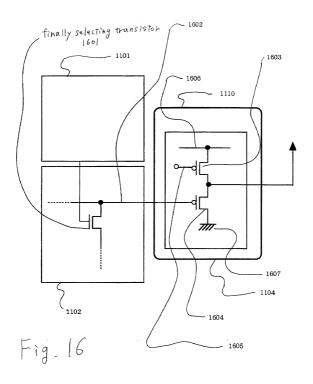


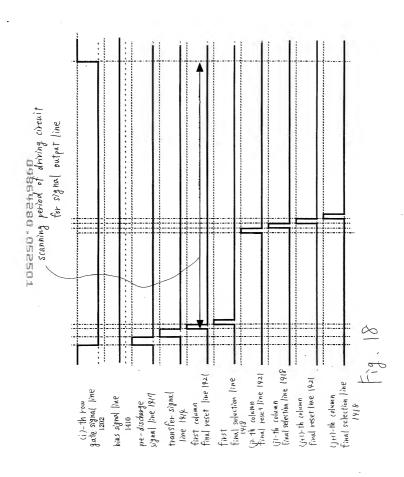
Fig. 14

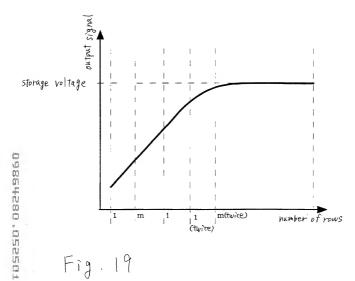




<b></b>						
one frame period	storage period of pixels of (i-1)-th row	storage period of pixels	storage period of pixels	storage period of pixels of (i-i)-th tow (twice)	storage period of pixels of (1)-th You (twice)	standa period of plants
one frame period	1-1行目の画素の雑種時間	行目の画来の蓄積時間	十1行目の画楽の諸徴時間			
The state of the s	reset signal line	(i)-th row reset signal line. 1205	(it) - th row reset signal line 1205	(i-1)-th rew gate signal line 1202	(i)-th row gate signal line 1202	citt) -th rew gate signal line 1202

F19, 17





<b>A</b>						
one Frame period storage period of (i-1). Th tow pixels	starage period of (1) th row pixels	Slavage period of (i+1). th row pixels				
nad line	(i) th vow test symmetime		(i-i) -th row gate synal line 1202	(i)-th row gate signal line 1202	(i+1)—th row gate signal line 1202	

Fig.20

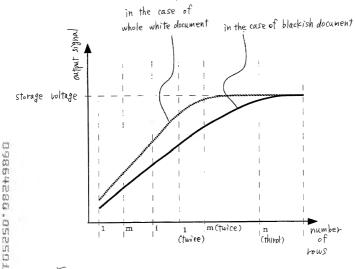


Fig. 21

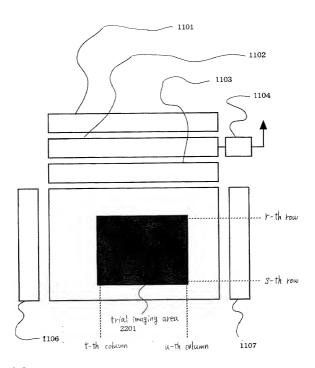
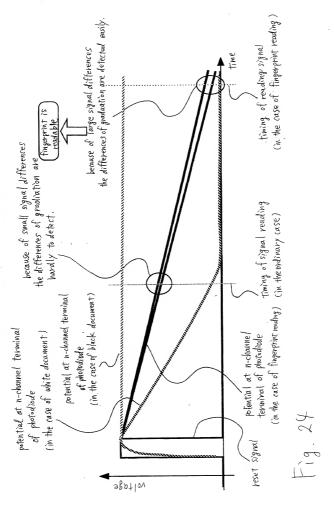
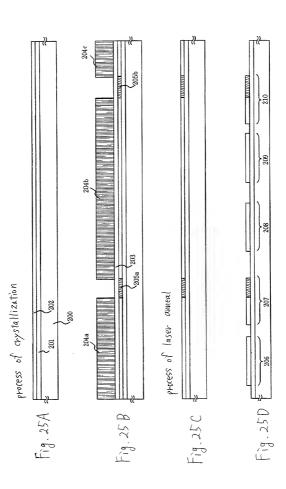
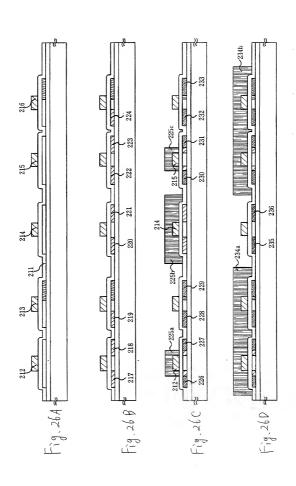
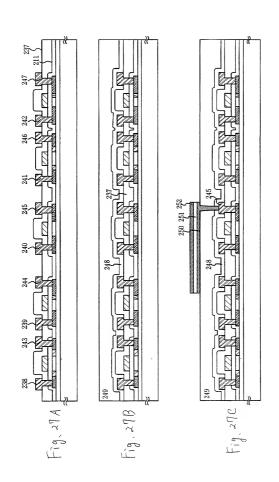


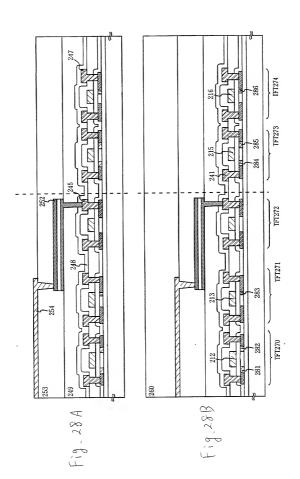
Fig. 22











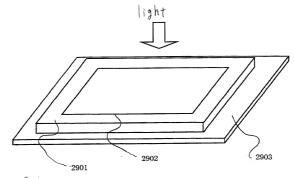
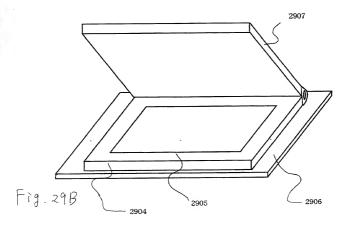
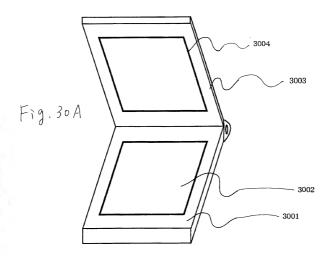
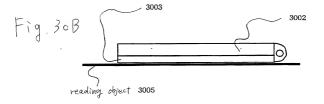


Fig. 29 A







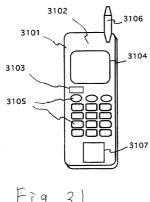


Fig. 31